

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Protigal et al.

Serial No.: 08/671,248

Filed: June 27, 1996

For: MEMORY MODULE HAVING ON-CHIP SURGE CAPACITORS

Examiner: Unknown

Group Art Unit: 2511

Attorney Docket No.: 2898US (88-070.5)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

09/20/96

Date of Deposit

Signature of registered practitioner or other person having reasonable basis to expect mailing to occur on date of deposit shown pursuant to 37 C.F.R. § 1.8(a)(1)(ii)

Joseph A. Walkowski

Typed/printed name of person whose signature is contained above

POWER OF ATTORNEY, REVOCATION OF PRIOR
POWER OF ATTORNEY (37 C.F.R. § 1.36) and REQUEST TO
CHANGE CORRESPONDENCE ADDRESS (37 C.F.R. § 1.33(d))
with STATEMENT PURSUANT TO 37 C.F.R. § 3.73

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

As assignee of the entire interest in the above-identified application, all powers of attorney previously given herein are HEREBY REVOKED and the following attorneys are hereby appointed to prosecute and transact all business in the U.S. Patent and Trademark Office in connection herewith.

David V. Trask, Reg. No. 22,012
William S. Britt, Reg. No. 20,969
Thomas J. Rossa, Reg. No. 26,799
Laurence B. Bond, Reg. No. 30,549
Joseph A. Walkowski, Reg. No. 28,765
James R. Duzan, Reg. No. 28,393
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Robert G. Winkle, Reg. No. 37,474
Patrick McBride, Reg. No. 39,295
Edgar R. Cataxinos, Reg. No. 39,931
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Michael L. Lynch, Reg. No. 30,871
Lia M. Pappas, Reg. No. 34,095

In accordance with 37 C.F.R. § 1.33(d), please change the address for all purposes in connection with the above-identified patent and direct all communications to:

JOSEPH A. WALKOWSKI
TRASK, BRITT & ROSSA
P. O. Box 2550
Salt Lake City, Utah 84110
(801) 532-1922

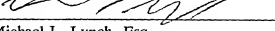
Pursuant to 37 C.F.R. § 3.73, the undersigned representative of the Assignee has reviewed the evidentiary documents, specifically the Assignment to Micron Technology, Inc. recorded on January 10, 1994 at Reel 6848, Frames 0725, and certifies that to the best of his knowledge and belief, title remains in the name of Micron Technology, Inc. as Assignee of record of the entire interest in the above-identified patent application.

The undersigned further avers that he is empowered to make and sign the foregoing certification on behalf of the Assignee, and to take the action set forth herein on behalf of the Assignee, pursuant to a resolution of its Board of Directors.

Respectfully submitted,

MICRON TECHNOLOGY, INC.

Dated: Sept 17, 1996

By: 
Michael L. Lynch, Esq.
Reg. No. 30,871
Chief Patent Counsel, an authorized
representative empowered to grant the
foregoing power of attorney

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Protigal et al.

Serial No.: 08/965,741

Filed: November 7, 1997

Title: INTEGRATED CIRCUIT MODULE HAVING ON-CHIP SURGE CAPACITORS

Examiner: N. Stanley

Group Art Unit: 2511

Attorney Docket No.: 2898.1US (88-070.6)

**POWER OF ATTORNEY BY ASSIGNEE
AND CERTIFICATE UNDER 37 CFR § 3.73(b)**Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

MICRON TECHNOLOGY, INC., assignee of the entire right, title and interest by assignment from the inventor(s) in the above-identified application, hereby appoints the following attorneys and agents:

David V. Trask, Reg. No. 22,012
 Laurence B. Bond, Reg. No. 30,549
 Allen C. Turner, Reg. No. 33,041
 Robert G. Winkle, Reg. No. 37,474
 Kenneth E. Horton, Reg. No. 39,481
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 Joseph A. Walkowski, Reg. No. 28,765
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 Edgar R. Cataxinos, Reg. No. 39,931
 Kenneth Booth, Reg. No. P-43,342

as its attorneys with full power of substitution to prosecute this application and to transact all business in the U.S. Patent and Trademark Office in connection therewith.

The above-identified assignee hereby elects, pursuant to 37 C.F.R. § 3.71, to conduct the prosecution of the above-identified patent application to the exclusion of the inventor(s).

A chain of title from the inventor(s) of the above-identified patent application to the above-identified assignee is shown:

[x] In an assignment recorded in the U.S. Patent and Trademark Office at Reel 6848, Frame 0726.

[] In an assignment filed herewith for recordation, a true copy of which is attached hereto.

The undersigned has reviewed the above-identified assignment and, to the best of his knowledge and belief, title is in the above-identified assignee.

The undersigned further avers that he is empowered to make and sign the foregoing certification on behalf of the above-identified assignee, and to take the action set forth herein on its behalf.

Please direct all communications regarding the above-identified application to:

Joseph A. Walkowski
 TRASK, BRITT & ROSSA
 P.O. Box 2550
 Salt Lake City, UT 84110
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Respectfully Submitted,

MICRON TECHNOLOGY, INC.

Date: April 6, 1998

By: 
 Michael L. Lynch, Esq.
 Reg. No. 30,871
 Chief Patent Counsel,
 MICRON TECHNOLOGY, INC.

DECLARATION

- As a below named inventor(s), I (we) hereby declare that:

My (our) residence, post office address and citizenship are as stated below next to my (our) name.

I (we) believe I (we) am (are) the original, first and sole inventor(s) of the subject matter which is claimed and for which a patent is sought on the invention entitled: MEMORY MODULE HAVING ON-CHIP SURGE CAPACITORS, ✓ the specification of which is attached hereto. *Serial no. 20294*

I (we) hereby state that I (we) have reviewed and understand the contents of the above identified specification, including the claims.

I (we) acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, # 1.56(a).

I (we) hereby claim foreign priority benefits under Title 35, United States Code, # 119 of any foreign applications(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

I (we) hereby claim the benefit under Title 35, United States Code, # 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, # 112, I (we) acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, # 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

I (we) hereby direct that all correspondence and telephone calls be addressed to Stanley N. Protigal, (Registration #28,657), and/or Angus C. Fox, III, (Registration #31,828), at 2805 East Columbia Road, Boise, Idaho, 83706 (208) 386-3781.

I (we) hereby declare that all statements made of my (our) own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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0076387-66604

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Inventor's Full Name: Kevin G. Duesman
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